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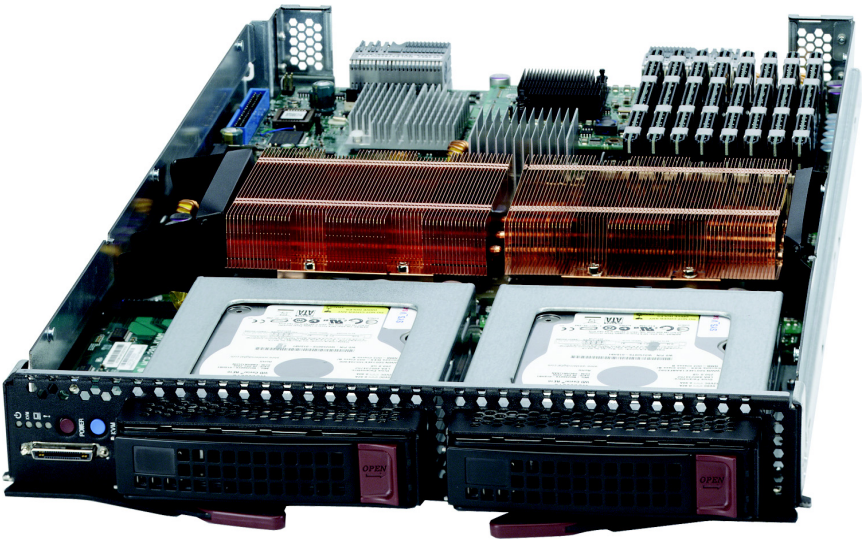
SBI-7125B-T1 Blade Module



BIOS Setup Manual

Revison 1.0

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Manual Revison 1.0

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SBI-7125B-T1
BIOS Setup Manual

1. Introduction

This document describes the Phoenix BIOS™ Setup utility for the SBI-7125B-T1 Intel Blade Module. The Phoenix ROM BIOS is stored in a flash chip and can be easily upgraded using a floppy disk-based program. See *Chapter 9* of the *Intel SuperBlade User's Manual* (MNL-0975) for further details.

2. Running Setup

NOTE: Default settings are in **bold** text unless otherwise noted.

The BIOS setup options described in this section are selected by choosing the appropriate text from the MAIN BIOS SETUP screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options.

When you first power on the computer, the BIOS is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1. By pressing <DELETE> immediately after turning the system on, or
2. When the message **Press the <Delete> key to enter Setup** appears briefly at the bottom of the screen during the POST, press the <DELETE> key to activate the main SETUP menu:

3. Main BIOS Setup

All main Setup options are described in this section.

Use the UP/DOWN arrow keys to move among the different settings in each menu. Use the LEFT/RIGHT arrow keys to change the options for each setting.

Press the <ESC> key to exit the CMOS SETUP menu. The next section describes in detail how to navigate through the menus.

Items that use sub-menus are indicated with the ▶ icon. With the item highlighted, press the <ENTER> key to access the submenu.

Menu options found in the MAIN BIOS SETUP menu are shown in the Table 1.

Table 1. Main BIOS Setup Menu Options

Menu Option	Description
System Time	To set the system date and time, key in the correct information in the appropriate fields. Then press the <Enter> key to save the data.
System Date	Using the arrow keys, highlight the month, day and year fields, and enter the correct data for the system date. Press the <Enter> key to save the data.
BIOS Date	The BIOS Date field displays the date when this version of the BIOS was built.
▶SATA Port 0/ SATA Port 1	These settings allow the user to set the parameters of the SATA Port 0 and 1 drives. Hit <Enter> to activate the following sub-menu screen for detailed options of these items, and to set the correct configurations accordingly. The items included in this submenu are shown in Table 2 below.

Table 2. SATA Port 0/SATA Port 1 Submenu Menu Options

Menu Option	Description
Type	Selects the type of SATA hard drive. Selecting User will allow the user to manually enter the parameters of the HDD. Selecting **Auto** will allow the BIOS to automatically configure the parameters of the HDD. Select CD-ROM if a CD-ROM drive is installed. Select ATAPI if a removable disk drive is installed.
Total Sectors	This item allows the user to specify the total number of sectors of the drive. This setting cannot be changed if the Type setting (above) has Auto selected.
Maximum Capacity	This item allows the user to specify the maximum capacity of the drive. This setting cannot be changed if the Type setting (above) has Auto selected.
Multi-Sector Transfer	This item allows the user to specify the number of sectors per block to be used in multi-sector transfer. The options are Disabled, 4 Sectors, 8 Sectors and **16 Sectors**.
LBA Mode Control	This item determines whether the BIOS will access the IDE Channel 0 Master Device via the LBA mode. The options are **Enabled** and Disabled.
32 Bit I/O	This option allows the user to enable or disable the 32-bit data transfer function. The options are Enabled and **Disabled**.
Transfer Mode	This option allows the user to set the transfer mode. The options are **Standard**, Fast PIO1, Fast PIO2, Fast PIO3, Fast PIO4, FPIO3/DMA1 and FPIO4/DMA2.
Ultra DMA Mode	This option allows the user to select Ultra DMA Mode. The options are **Disabled**, Mode 0, Mode 1, Mode 2, Mode 3, Mode 4 and Mode 5.
Parallel ATA	This setting allows the user to enable or disable Parallel ATA. The options are Enabled and **Disabled**.
Serial ATA	This setting allows the user to enable or disable Serial ATA. The options are **Enabled** and Disabled.
Native Mode Operation	Used to select the native mode for ATA. The options are **Auto** and Serial ATA.

Table 2. SATA Port 0/SATA Port 1 Submenu Menu Options (Continued)

Menu Option	Description
▶SATA Controller Mode	Select **Compatible** to allow the SATA and PATA drives to be automatically detected and placed in Legacy Mode by the BIOS. Select Enhanced to allow the SATA and PATA drives to be to be automatically detected and placed in Native IDE Mode. When the SATA Controller Mode is set to "Enhanced", it displays a submenu with the following menu options: **NOTE:** Enhanced mode is supported only by Windows 2000 OS and later versions.
Serial ATA (SATA) RAID Enable	Select Enable to enable Serial ATA RAID functions. The options are Enabled and **Disabled**. **NOTE:** For a Windows OS environment, use the RAID driver if this feature is set to Enabled. When this item is set to Enabled, the item: "ICH RAID Code Base" will be available for you to select either the Intel or the Adaptec Host RAID Controller. If this item is set to Disabled, the item SATA AHCI Enable will be available.
SATA AHCI	Select Enable to enable the Serial ATA Advanced Host Interface. The options are Enabled and **Disabled**. **WARNING:** Use caution when setting this function. This feature is for advanced programmers only.
System Memory	This display informs you how much system memory is recognized as being present in the system.
Extended Memory	This display informs you how much extended memory is recognized as being present in the system.

4. Advanced Setup

Choose **Advanced** from the BIOS Setup Utility main menu with the arrow keys. The items with a triangle beside them have sub menus that can be accessed by highlighting the item and pressing <ENTER>. Options for PIR settings are displayed by highlighting the setting option using the arrow keys and pressing <ENTER>. Table 3 contains a list of all menu options in the ADVANCED SETUP menu.

Table 3. Advanced Setup Menu Options

Submenu	Description
▶Boot Features	Access this submenu to make changes to boot features. See Table 4 for a list of menu options in this submenu.
▶Memory Cache	Access this submenu to make changes to settings for the memory cache. See Table 5 for a list of menu options in this submenu.
▶PCI Configuration	Access this submenu to make changes to settings for PCI devices. See Table 6 for a list of menu options in this submenu.
▶Advanced Chipset Control	Access this submenu to make changes to advanced chipset settings. See Table 7 for a list of menu options in this submenu. **WARNING:** Use caution when changing the Advanced settings. Incorrect values entered may cause a system malfunction. Also, a very high DRAM frequency or incorrect DRAM timing may cause system instability. When this occurs, revert to the default settings.

Table 3. Advanced Setup Menu Options (Continued)

Submenu	Description
▶Advanced Processor Options	Access this submenu to make changes to advanced processor option settings. See Table 8 for a list of menu options in this submenu.
▶I/O Device Configuration	Access this submenu to make changes to I/O device configuration settings. See Table 9 for a list of menu options in this submenu.
▶Console Redirection	Access this submenu to make changes to console redirection settings. See Table 10 for a list of menu options in this submenu.
▶Hardware Monitor	Access this submenu to make changes to the hardware monitor settings. See Table 11 for a list of menu options in this submenu.

Table 4. Boot Features Submenu Menu Options

Menu Option	Description
Quick Boot Mode	If enabled, this feature will speed up the POST (Power-On Self-Test) routine by skipping certain tests after the computer is turned on. The settings are **Enabled** and Disabled. If Disabled, the POST routine will run at normal speed.
Quiet Boot Mode	This setting allows you to Enable or **Disable** the diagnostic screen during boot-up.
POST Errors	Enabling this setting pauses and displays the Setup entry or resume boot prompt if an error occurs on boot. If disabled, the system will always attempt to boot. The settings are **Enabled** and Disabled.
ACPI Mode	Use this setting to determine if you want to employ ACPI (Advanced Configuration and Power Interface) power management on your system. The options are Yes and No.
Power Button Behavior	If set to Instant-Off, the system will power off immediately as soon as the user hits the power button. If set to 4-sec. override, the system will power off when the user presses the power button for 4 seconds or longer. The options are Instant-Off and **4-sec override**.
Power Loss Control	This setting allows you to choose how the system will react when power returns after an unexpected loss of power. The options are **Stay Off**, Power On and **L**ast State.
Summary Screen	This setting allows you to **Enable** or Disable the summary screen, which displays the system configuration during bootup.

Table 5. Memory Cache Submenu Menu Options

Menu Option	Description
Cache System BIOS Area	This setting allows you to designate a reserve area in the system memory to be used as a system BIOS buffer into which the BIOS will write (cache) its data. Select **Write Protect** to enable this function, and this area will be reserved for BIOS ROM access only. Select "Uncached" to disable this function and make this area available for other devices.
Cache Video BIOS Area	This setting allows you to designate a reserve area in the system memory to be used as a Video BIOS buffer into which the BIOS will write (cache) its data. Select **Write Protect** to enable the function and this area will be reserved for Video BIOS ROM access only. Select "Uncached" to disable this function and make this area available for other devices.

Table 5. Memory Cache Submenu Menu Options (Continued)

Menu Option	Description
Cache Base 0-512k	If enabled, this feature will allow the data stored in the base memory area (block 0-512k) to be cached (written) into a buffer, a storage area in the static DROM (SDROM) or to be written into the L1/L2/L3 cache in the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 0-512k. Select **Write Back** to allow the CPU to write data back directly from the buffer without writing data to the system memory for faster CPU operation.
Cache Base 512k-640k	If enabled, this feature will allow the data stored in memory area 512K-640k to be cached (written) into a buffer, a storage area in the static DROM (SDROM) or written into the L1/L2/L3 cache in the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 0-512k. Select **Write Back** to allow the CPU to write data back directly from the buffer without writing data to the system memory for faster CPU operation.
Cache Extended Memory Area	If enabled, this feature will allow the data stored in the extended memory area to be cached (written) into a buffer, a storage area in the static DROM (SDROM) or written into the L1/L2/L3 cache inside the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 0-512k. Select **Write Back** to allow CPU to write data back directly from the buffer without writing data to the system memory for faster CPU operation.
Discrete MTRR Allocation	If enabled, MTRRs (Memory Type Range Registers) are configured as distinct, separate units and cannot be overlapped. If enabled, the user can achieve better graphic effects when using a Linux graphic driver that requires the write-combining configuration with 4GB or more memory. The options are Enabled and **Disabled**.

Table 6. PCI Configuration Submenu Menu Options

Menu Option	Description
Onboard GLAN1/ Onboard GLAN2 (Gigabit- LAN) OPROM Configure	Enabling this option provides the capability to boot from an Ethernet port. The options are **Enabled** and Disabled.
Default Primary Video Adapter	Choose the default video adapter. The options are **Onboard** and Other.
Emulated IRQ Solution	Choose the emulated IRQ solution. The options are Enabled and **Disabled**.
PCI-E I/O Performance	Choose between **Payload 256B** (with coalesce disabled) and Coalesce (with a payload size of 128 bytes).
PCI Parity Error Forwarding	Enabling logs PCI SERR/PERR error events in BIOS and IPMI. The options are Enabled and **Disabled**.

Table 6. PCI Configuration Submenu Menu Options (Continued)

Menu Option	Description
ROM Scan Ordering	Determines what kind of option ROM activates first. The options are **Onboard First** and Addon First.
PCI Fast Delayed Transaction	Enabling improves heavy DMA transfer for 32-bit PCI multimedia cards. The options are Enabled and **Disabled**.
Reset Configuration Data	If set to Yes, this setting clears the Extended System Configuration Data (ESCD) area. The options are **Yes** and No.
Large Disk Access Mode	This setting determines how large hard drives are to be accessed. The options are **DOS** or Other (for Unix, Novelle NetWare and other operating systems).

Table 7. Advanced Chipset Control Submenu Menu Options

Menu Option	Description
SERR Signal Condition	This setting specifies the ECC Error conditions that an SERR# is to be asserted. The options are None, **Single Bit**, Multiple Bit and Both.
4GB PCI Hole Granularity	This feature allows you to select the granularity of PCI hole for PCI slots. If MTRRs are not enough, this option may be used to reduce MTRR occupation. The options are **256 MB**, 512 MB, 1GB and 2GB.
Memory Branch Mode	This option allows the BIOS to enumerate Host Mode for Device 16, Function 1, Reg. 40h bit 16 and Reg. 58h [14]. The options are **Interleave**, Sequential, Mirror and Single Channel 0.
Branch 0 Rank Interleave	Selects the Branch 0 rank interleave. The options are 1:1, 2:1 and **4:1**.
Branch 0 Rank Sparing	Enable to enable the sparing feature for Branch 0 Rank. The options are Enabled and **Disabled**.
Branch 1 Rank Interleave	Selects the Branch 1 rank interleave. The options are 1:1, 2:1 and **4:1**.
Branch 1 Rank Sparing	Enable to enable the sparing feature for Branch 1 Rank. The options are Enabled and **Disabled**.
Enhanced x8 Detection	Select enabled to enable Enhanced x8 DRAM UC Error Detection. The options are **Enabled** and Disabled.
High Bandwidth FSB	Select **Enabled** to enable a high bandwidth FSB or Disable to disable it.
High Temp DRAM OP	Select Enabled to enable a high temp DRAM OP or **Disable** to disable it.
ABM Thermal Sensor	Select Enabled to enable the ABM thermal sensor or **Disable** to disable it.
Thermal Throttle	Select Enabled to enable the Thermal Throttle function or **Disable** to disable it.
Global Activation Throttle	Select Enabled to enable the Global Activation Throttle function or **Disable** to disable it.
Crystal Beach Feature	Enabling this creates memory-mapped accesses to the Crystal Beach configuration space located in Device 8, Fn 0 and Fn 1. The options are **Enabled** and Disabled.
Route Port 80h Cycles to	This feature allows the user to decide which bus to send debug information to. The options are PCI and **LPC**.

Table 7. Advanced Chipset Control Submenu Menu Options (Continued)

Menu Option	Description
Clock Spectrum Feature	If Enabled, the BIOS will monitor the level of Electromagnetic Interference caused by the components and will attempt to decrease the interference whenever needed. The options are Enabled and **Disabled**.
High Precision Event Timer	Use this setting to Enable or Disable HPET support. The options are Yes and **No**.
USB Function	Select Enabled to enable all USB devices specified. The options are **Enabled** and Disabled.
Legacy USB Support	This setting allows you to enable support for Legacy USB devices. The options are **Enabled** and Disabled.

Table 8. Advanced Processor Options Submenu Menu Options

Menu Option	Description
CPU Speed	This is a display that indicates the speed of the installed processor.
Frequency Ratio	Selects the internal frequency multiplier of the CPU(s). Options are **Default**, x6 and x7.
Core Multi-Processing (Available when supported by the CPU)	Determines whether the 2nd CPU core is enabled. The options are **Enabled** and Disabled.
Machine Checking (Available when supported by the CPU)	Set to Enabled to activate Machine Checking and allow the CPU to detect and report hardware (machine) errors via a set of model-specific registers (MSRs). The options are **Enabled** and Disabled.
Thermal Management 2 (Available when supported by the CPU)	Set to **Enabled** to use Thermal Management 2 (TM2), which will lower the CPU voltage and frequency when the CPU temperature reaches a predefined overheat threshold. Set to Disabled to use Thermal Manager 1 (TM1), which allows CPU clocking to be regulated via CPU Internal Clock modulation when the CPU temperature reaches the overheat threshold.
C1 Enhanced Mode (Available when supported by the CPU)	Set to Enabled to enable Enhanced Halt State to lower the CPU voltage/ frequency to prevent overheating. The options are Enabled and **Disabled**. Refer to the Intel web site for detailed information.
Execute Disable Bit	Set to **Enable** to allow the processor to classify areas in memory where an application code can execute and where it cannot, and thus preventing a worm or a virus from inserting and creating a flood of codes to overwhelm the processor or damage the system during an attack. **Note:** this feature is available when your OS and your CPU support the Execute Disable Bit function. For more information, please refer to the Intel and Microsoft web sites.
Adjacent Cache Line Prefetch (Available when supported by the CPU)	The CPU fetches the cache line for 64 bytes if this option is set to Disabled. The CPU fetches both cache lines for 128 bytes as comprised if Enabled. Options are Enabled and **Disabled**.
Hardware Prefetcher	Select to **Enable** or Disable hardware prefetching.

Table 8. Advanced Processor Options Submenu Menu Options (Continued)

Menu Option	Description
Direct Cache Access	This is a system level protocol used in a multi-processor systems to improve I/O network performance. Options are Enabled and **Disabled**.
Intel (R) Virtualization Technology	Select Enabled to use the feature of Virtualization Technology. The options are Enabled and **Disabled**.
Intel EIST Support	EIST is used to allow the CPU state to dynamically change based on the system load. The options are Enabled and **Disabled**. (Native mode support only.)

Table 9. I/O Device Configuration Submenu Menu Options

Menu Option	Description
KBC Clock Input	This setting allows you to select clock frequency for KBC. The options are 6MHz, 8MHz, **12MHz**, and 16MHz.
▶Serial Port A	This setting allows you to assign control of serial port A. The options are **Enabled** (user defined), Disabled, and Auto (BIOS or OS controlled).
Base I/O Address	This setting allows you to select the base I/O address for serial port A. The options are **3F8**, 2F8, 3E8, and 2E8.
Interrupt	This setting allows you to select the IRQ (interrupt request) for serial port A. The options are IRQ3 and **IRQ4**.
▶Serial Port B	This setting allows you to assign control of serial port B. The options are **Enabled** (user defined), Disabled, Auto (BIOS controlled) and OS Controlled.
Mode	This setting allows you to set the type of device that will be connected to serial port B. The options are **Normal** and IR (for an infrared device).
Base I/O Address	This setting allows you to select the base I/O address for serial port B. The options are 3F8, **2F8**, 3E8 and 2E8.
Interrupt	This setting allows you to select the IRQ (interrupt request) for serial port B. The options are **IRQ3** and IRQ4.
I^2C Bus Switch	This setting allows you to switch on or off the I^2C bus. The options are **Auto** and Disabled.
▶DMI Event Logging	Access the submenu to make changes to the following settings.
Event Log Validity	This is a display to inform you of the event log validity. It is not a setting.
Event Log Capacity	This is a display to inform you of the event log capacity. It is not a setting.
View DMI Event Log	Highlight this item and press <Enter> to view the contents of the event log.
Event Logging	This setting allows you to **Enable** or Disable event logging.
ECC Event Logging	This setting allows you to **Enable** or Disable ECC event logging.
Mark DMI Events as Read	Highlight this item and press <Enter> to mark the DMI events as read.
Clear All DMI Event Logs	Select Yes and press <Enter> to clear all DMI event logs. The options are Yes and **No**.

Table 10. Console Redirection Submenu Menu Options

Menu Option	Description
COM Port Address	This item allows you to specify to redirect the console to Onboard COM A or Onboard COM B. This setting can also be **Disabled**.
BAUD Rate	This item allows you to select the BAUD rate for console redirection. The options are 300, 1200, 2400, 9600, **19.2K**, 38.4K, 57.6K, and 115.2K.
Console Type	This item allows you to choose from the available options to select the console type for console redirection. The options are VT100, VT100 (8bit), PC-ANSI (7bit), **PC ANSI**, VT100+, and VT-UTF8.
Flow Control	This item allows you to choose from the available options to select the flow control for console redirection. The options are: None, XON/XOFF, and **CTS/ RTS**.
Console Connection	This item allows you to choose select the console connection: either **Direct** or Via Modem.
Continue CR after POST	Choose whether to continue with console redirection after the POST routine. The options are On and **Off**.

Table 11. Hardware Monitor Submenu Menu Options

Menu Option	Description
CPU Temperature Threshold	This option allows the user to set a CPU temperature threshold that will activate the alarm system when the CPU temperature reaches this pre-set temperature threshold. The options are 70°C, **75°C**, 80°C and 85°C. The hardware monitor provides the following temperature data: • PECI Agent 1 Temperature • PECI Agent 2 Temperature • System Temperature
Voltage Monitoring	The following voltages are displayed: • VcoreA • VcoreB • +1.8V • P1V5 • +3.3V • +12V • 5Vsb • 5VDD • P_VTT • Vbat

5. Security

Choose **Security** from the Phoenix BIOS Setup Utility main menu with the arrow keys. Security setting options are displayed by highlighting the setting using the arrow keys and pressing <ENTER>. All Security BIOS settings are described in Table 12 below.

Table 12. Security Menu Options

Menu Option	Description
Supervisor Password Is:	This displays whether a supervisor password has been entered for the system. Clear means such a password has not been used and Set means a supervisor password has been entered for the system.
User Password Is:	This displays whether a user password has been entered for the system. Clear means such a password has not been used and Set means a user password has been entered for the system.
Set Supervisor Password	When the item "Set Supervisor Password" is highlighted, hit the <Enter> key. When prompted, type the Supervisor's password in the dialogue box to set or to change supervisor's password, which allows access to the BIOS.
Set User Password	When the item "Set User Password" is highlighted, hit the <Enter> key. When prompted, type the user's password in the dialogue box to set or to change the user's password, which allows access to the system at boot-up.
Password on Boot	This setting allows you to require a password to be entered when the system boots up. The options are Enabled (password required) and Disabled (password not required).

6. Boot

Choose **Boot** from the Phoenix BIOS Setup Utility main menu with the arrow keys. Highlighting a setting with a **+** or **-** will expand or collapse that entry. See details on how to change the order and specs of boot devices in the ITEM SPECIFIC HELP window.

Boot Priority Order/Excluded from Boot Order

Use the UP and DOWN arrow keys to select a device. Use a **<+>** key or a **<->** key to move the device up or down. Use the **<f>** key or the **<r>** key to specify the devices. You can also use the keys indicated above to specify the priority of boot order of a device or to move items from the category of EXCLUDED FROM BOOT ORDER to the category of BOOT PRIORITY ORDER and vise versa. See details on how to change the priority of boot order of devices in the ITEM SPECIFIC HELP window.

7. Exit

Choose EXIT from the Phoenix BIOS Setup Utility main menu with the arrow keys. All Exit BIOS settings are described in Table 13 below.

Table 13. Exit Menu Options

Menu Option	Description
Exit Saving Changes	Highlight this item and hit <Enter> to save any changes you made and to exit the BIOS Setup utility.
Exit Discarding Changes	Highlight this item and hit <Enter> to exit the BIOS Setup utility without saving any changes you may have made.

Table 13. Exit Menu Options (Continued)

Menu Option	Description
Load Setup Defaults	Highlight this item and hit <Enter> to load the default settings for all items in the BIOS Setup. These are the safest settings to use.
Discard Changes	Highlight this item and hit <Enter> to discard (cancel) any changes you made. You will remain in the Setup utility.
Save Changes	Highlight this item and hit <Enter> to save any changes you made. You will remain in the Setup utility.

Notes